



LIT

APPLIED SCIENCE,
ENGINEERING
& TECHNOLOGY

**Department of Electrical and Electronic
Engineering
Alternate Assessment Timetable
Autumn 2020**

Contents

Industrial Automation and Robotic Systems – Year 1	3
Industrial Automation and Robotic Systems – Year 2	4
Industrial Automation and Robotic Systems – Year 3	5
Industrial Automation and Robotic Systems – Year 4 – Semester One	6
Industrial Automation and Robotic Systems – Year 4 – Semester Two	7
Electrical Engineering Year 1	8
Renewable and Electrical Energy Engineering Year 2	9
Electrical Engineering Year 3	10
Renewable and Electrical Energy Engineering Year 3	10
Electrical Engineering Year 4 – Semester 1	11
Renewable and Electrical Energy Engineering Year 4 – Semester 1	11
Electrical Engineering Year 4 – Semester 2	12
Renewable and Electrical Energy Engineering Year 4 – Semester 2	12
Electronic Engineering Year 1	13
Electronic Engineering Year 3	14
Electronic Engineering Year 4 – Semester 1	15
Electronic Engineering Year 4 – Semester 2	16
Industrial Electrical Engineering Apprenticeship – EIEAM	17
Manufacturing Engineering Apprenticeship - EMEAW	18
Electrical Technology, Stage 3 (Flexible Learning programme)	19
Manufacturing Technology - Stage 3 (Flexible Learning programme)	20

Timetable for Alternative Assessment

Industrial Automation and Robotic Systems – Year 1

Due to the Covid-19 pandemic, the August 2020 exam session at LIT has had to be cancelled. In each case, alternative arrangements have been put in place; You will/have been given detailed instructions as to how this process will work by your lecturer – all correspondence will be via your student email.

If you have not received any information about this process it is your responsibility to contact the lecturer as soon as possible.

You must complete assessments for any modules which you have not yet successfully completed, as per your June 2020 exam results.

	Industrial Electronics 1	Mechanical Maintenance Principles	Engineering Maths 1	Engineering Science	Electrical Technology
Alternative Assessment	Timed open book exam	Timed open book exam	Timed open book exam	Timed open book exam	Timed open book exam
Date	1 st Sept 2020	1st Sept 2020	2 nd Sept 2020	3 rd Sept 2020	4 th Sept 2020
Time	9.30 AM start	2.30 PM start	9.30 AM start	9.30 AM start	9.30 AM start

PLEASE STAY IN CONTACT WITH YOUR LECTURER AND PLEASE **ONLY LISTEN TO OFFICIAL INFORMATION.**

Timetable for Alternative Assessment

Industrial Automation and Robotic Systems – Year 2

Due to the Covid-19 pandemic, the August 2020 exam session at LIT has had to be cancelled. In each case, alternative arrangements have been put in place; You will/have been given detailed instructions as to how this process will work by your lecturer – all correspondence will be via your student email.

If you have not received any information about this process it is your responsibility to contact the lecturer as soon as possible.

You must complete assessments for any modules which you have not yet successfully completed, as per your June 2020 exam results.

	Industrial Electronics and C-programming	Engineering Maths 2
Alternative Assessment	Moodle Quiz	Timed open book exam
Date	1 st Sept 2020	2 nd Sept 2020
Time	9.30 AM start	9.30 AM start

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Timetable for Alternative Assessment

Industrial Automation and Robotic Systems – Year 3

Due to the Covid-19 pandemic, the August 2020 exam session at LIT has had to be cancelled. In each case, alternative arrangements have been put in place; You will/have been given detailed instructions as to how this process will work by your lecturer – all correspondence will be via your student email.

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You must complete assessments for any modules which you have not yet successfully completed, as per your June 2020 exam results.

	Advanced PLC's, HMI and Scada	Eng Maths 3	Applied C Programming
Alternative Assessment	Timed book exam	Timed book exam	Timed open book exam
Date	1 st Sept. 2020	2 nd Sept 2020	3 rd Sept 2020
Time	9.30 AM start	9.30 AM start	9.30 AM start

PLEASE STAY IN CONTACT WITH YOUR LECTURER AND PLEASE **ONLY LISTEN TO OFFICIAL INFORMATION.**

Timetable for Alternative Assessment

Industrial Automation and Robotic Systems – Year 4 – Semester One

Due to the Covid-19 pandemic, the August 2020 exam session at LIT has had to be cancelled. In each case, alternative arrangements have been put in place; You will/have been given detailed instructions as to how this process will work by your lecturer – all correspondence will be via your student email.

If you have not received any information about this process it is your responsibility to contact the lecturer as soon as possible.

You must complete assessments for any modules which you have not yet successfully completed, as per your Semester One and Semester Two exam results.

	Statistical Process Control	Advanced Automation Design	Control Eng 1
Alternative Assessment	Timed book exam	Timed open book exam	Timed open book exam
Date	1 st Sept 2020	1 st Sept 2020	2 nd Sept 2020
Time	9.30 AM start	2.30 PM start	9.30 AM start

Timetable for Alternative Assessment

Industrial Automation and Robotic Systems – Year 4 – Semester Two

Due to the Covid-19 pandemic, the August 2020 exam session at LIT has had to be cancelled. In each case, alternative arrangements have been put in place; You will/have been given detailed instructions as to how this process will work by your lecturer – all correspondence will be via your student email.

If you have not received any information about this process it is your responsibility to contact the lecturer as soon as possible.

You must complete assessments for any modules which you have not yet successfully completed, as per your Semester One and Semester Two exam results.

	Computer System Validation	Applied Statistical Analysis	Control Engineering 2	Advanced SCADA & Industrial Networks
Alternate Assessment	Timed open book exam	Timed open book exam	Timed open book exam	Practical Exam (8A215)
Date	3 rd Sept 2020	3 rd Sept 2020	4 th Sept 2020	7 th Sept 2020
Time	9.30 AM start	2.30 PM start	2.30 PM start	10am to 12 noon

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Timetable for Alternative Assessment

Electrical Engineering Year 1

Due to the Covid-19 pandemic, the August 2020 exam session at LIT has had to be cancelled. In each case, alternative arrangements have been put in place; You will/have been given detailed instructions as to how this process will work by your lecturer – all correspondence will be via your student email.

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You must complete assessments for any modules which you have not yet successfully completed, as per your June 2020 exam results.

	Electrical Theory for Installation	Engineering Maths 1	Engineering Science	Intro to Energy Systems	Electrical Technology
Alternative Assessment	Timed open book exam	Timed open book exam	Timed open book exam	Timed open book exam	Timed open book exam
Date	1 st Sept 2020	2 nd Sept 2020	3 rd Sept 2020	3rd Sept 2020	4 th Sept 2020
Time	9.30 AM start	9.30 AM start	9.30 AM start	2.30 PM start	9.30 AM start

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Timetable for Alternative Assessment

Renewable and Electrical Energy Engineering Year 2

Due to the Covid-19 pandemic, the August 2020 exam session at LIT has had to be cancelled. In each case, alternative arrangements have been put in place; You will/have been given detailed instructions as to how this process will work by your lecturer – all correspondence will be via your student email.

If you have not received any information about this process it is your responsibility to contact the lecturer as soon as possible.

You must complete assessments for any modules which you have not yet successfully completed, as per your June 2020 exam results.

	Engineering Mathematics 2
Alternative Assessment	Timed open book exam
Date	2 nd Sept 2020
Time	9.30 AM start

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Timetable for Alternative Assessment

Electrical Engineering Year 3

Renewable and Electrical Energy Engineering Year 3

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You must complete assessments for any modules which you have not yet successfully completed, as per your June 2020 exam results.

	Adv. PLCs, HMI and Scada	Elect Machines & Power Distribution	Electrical Planning and Layout	Maths for Electrical Engineers	Industrial Maint. And Energy Efficiency	Process instrumentation and calibration
Alternative Assessment	Timed book exam	Timed open book exam	Practical Exam	Timed book exam	Timed book exam	Practical Exam (8A209/10)
Date	1 st Sept 2020	2nd Sept 2020	2 nd Sept 2020	3 rd Sept 2020	4 th Sept 2020	7 th Sept. 2020
Time	9.30 AM start	9.30 AM start	2.30pm start	9.30 AM start	9.30 AM start	10am – 12 noon

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Timetable for Alternative Assessment

Electrical Engineering Year 4 – Semester 1

Renewable and Electrical Energy Engineering Year 4 – Semester 1

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[If you have not received any information about this process it is your responsibility to contact the lecturer as soon as possible.](#)

You must complete assessments for any modules which you have not yet successfully completed, as per your Semester One and Two exam results.

	Statistical Process Control	Adv. Automation Design	Elect. Power Systems 1
Alternative Assessment	Timed book exam	Timed open book exam	Timed open book exam and assignment
Date	1 st Sept 2020	1 st Sept 2020	2 nd Sept 2020
Time	9.30 AM start	2.30 PM start	9.30 AM start

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Timetable for Alternative Assessment

Electrical Engineering Year 4 – Semester 2

Renewable and Electrical Energy Engineering Year 4 – Semester 2

Due to the Covid-19 pandemic, the August 2020 exam session at LIT has had to be cancelled. In each case, alternative arrangements have been put in place; You will/have been given detailed instructions as to how this process will work by your lecturer – all correspondence will be via your student email.

[If you have not received any information about this process it is your responsibility to contact the lecturer as soon as possible.](#)

You must complete assessments for any modules which you have not yet successfully completed, as per your Semester One and Two exam results.

	Computer System Validation	Applied Statistical Analysis	Advanced SCADA & Industrial Networks
Alternative Assessment	Timed open book exam	Timed open book exam	Practical Exam (8A215)
Date	3 rd Sept 2020	3 rd Sept 2020	7 th Sept 2020
Time	9.30 AM start	2.30 PM start	10am to 12 noon

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Timetable for Alternative Assessments

Electronic Engineering Year 1

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You must complete assessments for any modules which you have not yet successfully completed, as per your June 2020 exam results.

	Analogue Electronics 1	Engineering Maths 1
Alternative Assessment	Timed open book exam	Timed open book exam
Date	1 st Sept. 2020	2 nd Sept. 2020
Time	9.30 AM start	9.30 AM start

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Timetable for Alternative Assessment

Electronic Engineering Year 3

Due to the Covid-19 pandemic, the August 2020 exam session at LIT has had to be cancelled. In each case, alternative arrangements have been put in place; You will/have been given detailed instructions as to how this process will work by your lecturer – all correspondence will be via your student email.

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You must complete assessments for any modules which you have not yet successfully completed, as per your June 2020 exam results.

	Digital Communications	MicroControllers and Interfacing	Eng Maths 3	Analog Electronics 3	Data Acquisition and Pc Interfacing
Alternative Assessment	Timed Open-Book exam	Timed Open-Book exam	Timed book exam	Timed Open Book exam	Timed Open-Book exam
Date	1 st Sept 2020	1 st Sept 2020	2 nd Sept 2020	2 nd Sept 2020	3 rd Sept 2020
Time	9.30 AM start	2.30 PM start	9.30 AM start	2.30 PM start	9.30 AM start

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Timetable for Alternative Assessment

Electronic Engineering Year 4 – Semester 1

Due to the Covid-19 pandemic, the August 2020 exam session at LIT has had to be cancelled. In each case, alternative arrangements have been put in place; You will/have been given detailed instructions as to how this process will work by your lecturer – all correspondence will be via your student email.

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You must complete assessments for any modules which you have not yet successfully completed, as per your Semester One and Two exam results.

	Advanced Digital Communications	Statistical Process Control	Electronic System design	HDL and Digital Design	Embedded Systems
Alternative Assessment	Repeat Assignment. Details communicated to students via email/Moodle.	Timed open book exam	Timed open book exam	Timed open book exam	Timed open book exam
Date	1 st Sept 2020	1 st Sept 2020	2 nd Sept 2020	2 nd Sept. 2020	3 rd Sept 2020
Time	2.30 PM start	9.30 AM start	9.30am start	2.30 pm start	9.30 AM start

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Timetable for Alternative Assessment

Electronic Engineering Year 4 – Semester 2

Due to the Covid-19 pandemic, the August 2020 exam session at LIT has had to be cancelled. In each case, alternative arrangements have been put in place; You will/have been given detailed instructions as to how this process will work by your lecturer – all correspondence will be via your student email.

If you have not received any information about this process it is your responsibility to contact the lecturer as soon as possible.

You must complete assessments for any modules which you have not yet successfully completed, as per your Semester One and Two exam results.

	Electronic System Analysis	Applied Statistical Analysis	Advanced Digital Comms and Networking	Real Time Embedded Systems
Alternative Assessment	Timed open book exam	Timed open book exam	Repeat Assignment. Details communicated to students via email/Moodle.	Timed open book exam
Date	1 st Sept 2020	3 rd Sept 2020	4 th Sept 2020	4 th Sept 2020
Time	2.30 PM start	2.30 PM start	9.30 AM start	2.30 PM start

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Timetable for Alternative Assessment

Industrial Electrical Engineering Apprenticeship – EIEAM

Due to the Covid-19 pandemic, the August 2020 exam session at LIT has had to be cancelled. In each case, alternative arrangements have been put in place; You will/have been given detailed instructions as to how this process will work by your lecturer – all correspondence will be via your student email.

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You must complete assessments for any modules which you have not yet successfully completed, as per your June 2020 exam results.

	HV & Distributed Electrical Systems	Electrical Planning and Layout
Alternative Assessment	Timed open book exam	Timed open book exam
Date	1 st Sept 2020	2 nd Sept 2020
Time	9.30 AM start	9.30 AM start

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Timetable for Alternative Assessment

Manufacturing Engineering Apprenticeship - EMEAW

Due to the Covid-19 pandemic, the August 2020 exam session at LIT has had to be cancelled. In each case, alternative arrangements have been put in place; You will/have been given detailed instructions as to how this process will work by your lecturer – all correspondence will be via your student email.

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You must complete assessments for any modules which you have not yet successfully completed, as per your June 2020 exam results.

	Electrical Science	Eng. Maths 1
Alternative Assessment	Timed open book exam	Timed open book exam
Date	1 st Sept 2020	2 nd Sept. 2020
Time	9.30 AM start	2.30 – 5.30pm (plus 30 minute upload time)

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Timetable for Alternative Assessment

Electrical Technology, Stage 3 (Flexible Learning programme)

Due to the Covid-19 pandemic, the August 2020 exam session at LIT has had to be cancelled. In each case, alternative arrangements have been put in place; You will/have been given detailed instructions as to how this process will work by your lecturer – all correspondence will be via your student email.

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You must complete assessments for any modules which you have not yet successfully completed, as per your Semester One and Two exam results.

	Electrical Machines	Elect Testing and Certification
Alternative Assessment	Timed open book exam	Timed open book exam
Date	1 st Sept 2020	1 st Sept 2020
Time	9.30 AM start	2.30 PM start

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Timetable for Alternative Assessment

Manufacturing Technology - Stage 3 (Flexible Learning programme)

Due to the Covid-19 pandemic, the August 2020 exam session at LIT has had to be cancelled. In each case, alternative arrangements have been put in place; You will/have been given detailed instructions as to how this process will work by your lecturer – all correspondence will be via your student email.

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You must complete assessments for any modules which you have not yet successfully completed, as per your Semester One and Two exam results.

	PLC's	Instrumentation and Calibration	Advanced PLCs	Mathematics 1
Alternative Assessment	Timed open book exam	Timed open book exam	Timed open book exam	Timed open book exam
Date	1 st Sept 2020	1 st Sept 2020	2 nd Sept 2020	2 nd Sept 2020
Time	9.30 AM start	2.30 PM start	9.30 AM start	2.30 PM start

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